

FIG.2

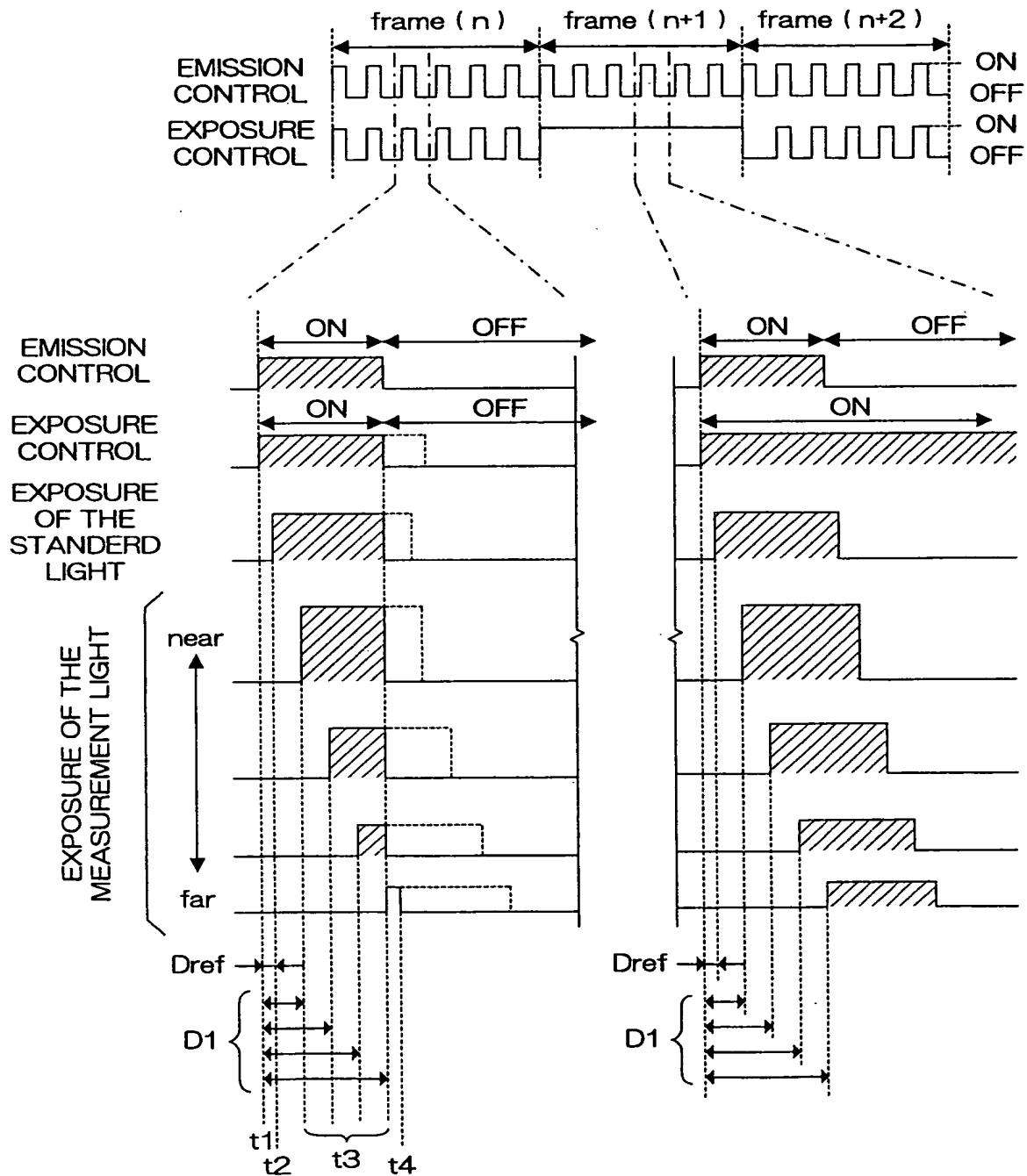


FIG.3

34 IMAGE PROCESSING CIRCUIT

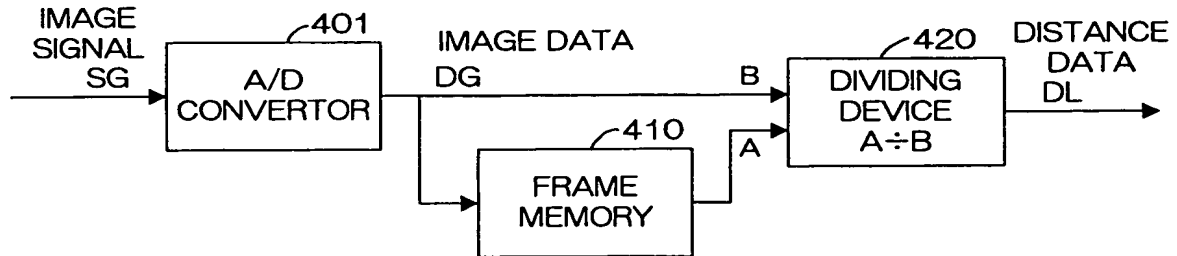


FIG.4(a)

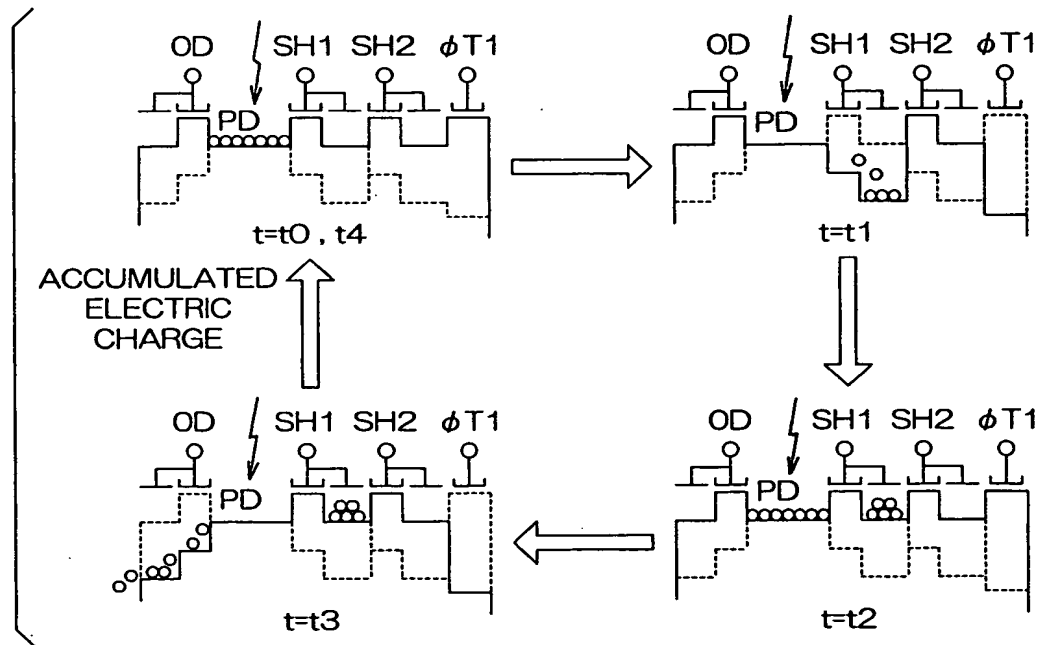


FIG.4(b)

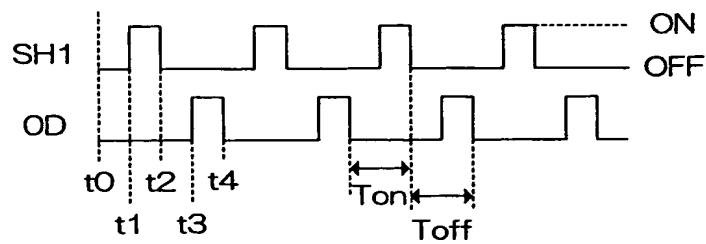
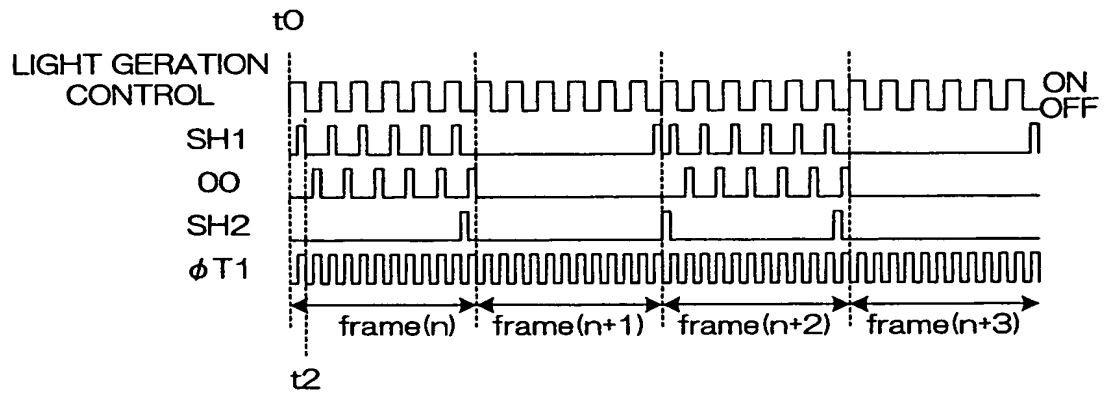


FIG.5



0985506-05.1601

FIG.6(a)

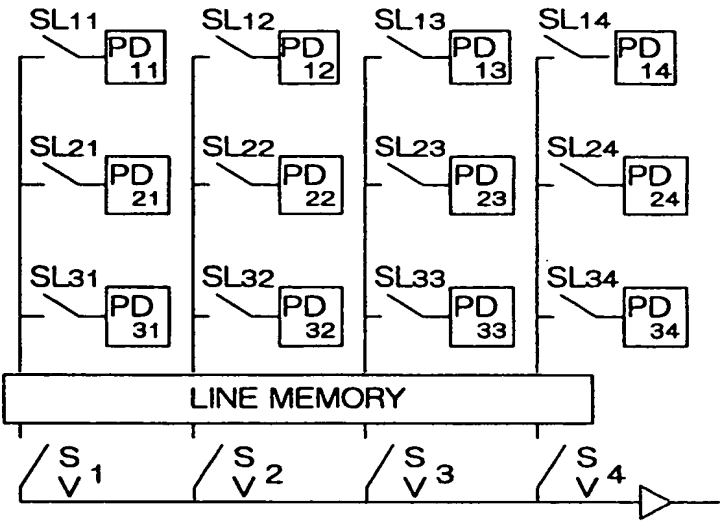


FIG.6(b)

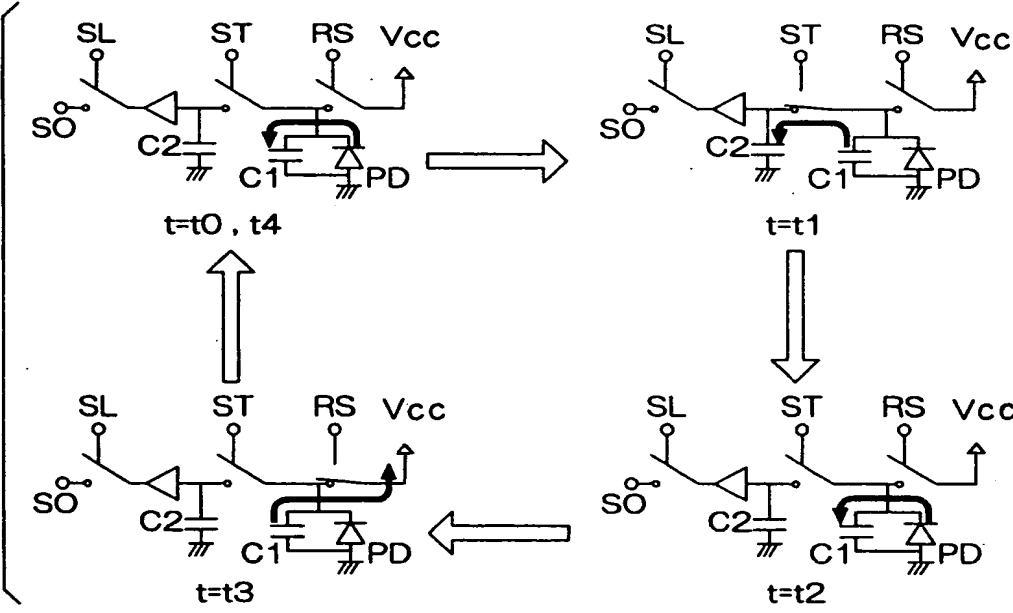


FIG.6(c)

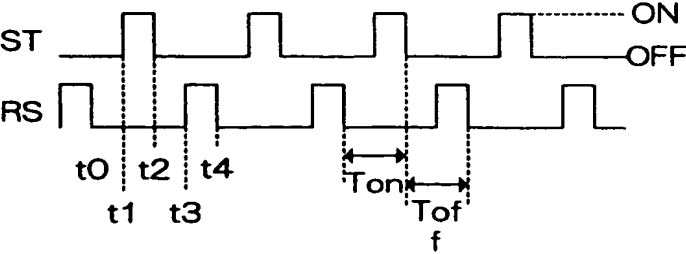
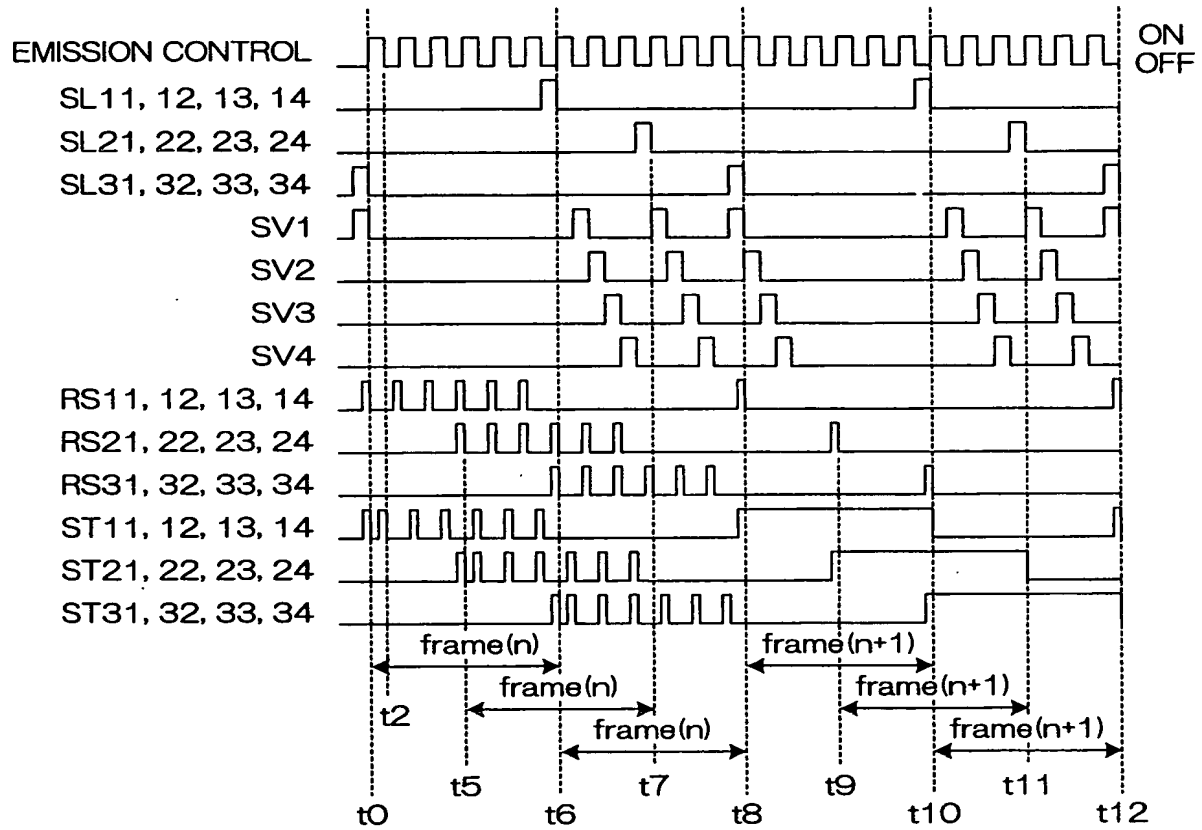


FIG.7



0985505-051601

FIG.8

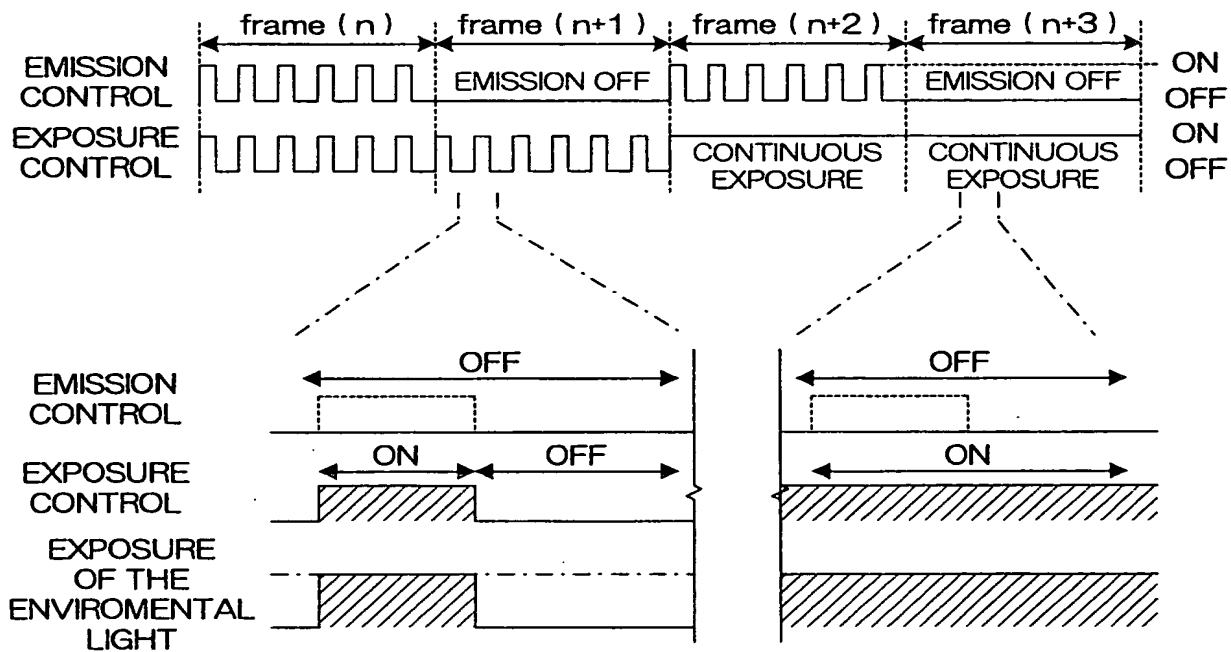


FIG.9

34b IMAGE PROCESSING CIRCUIT

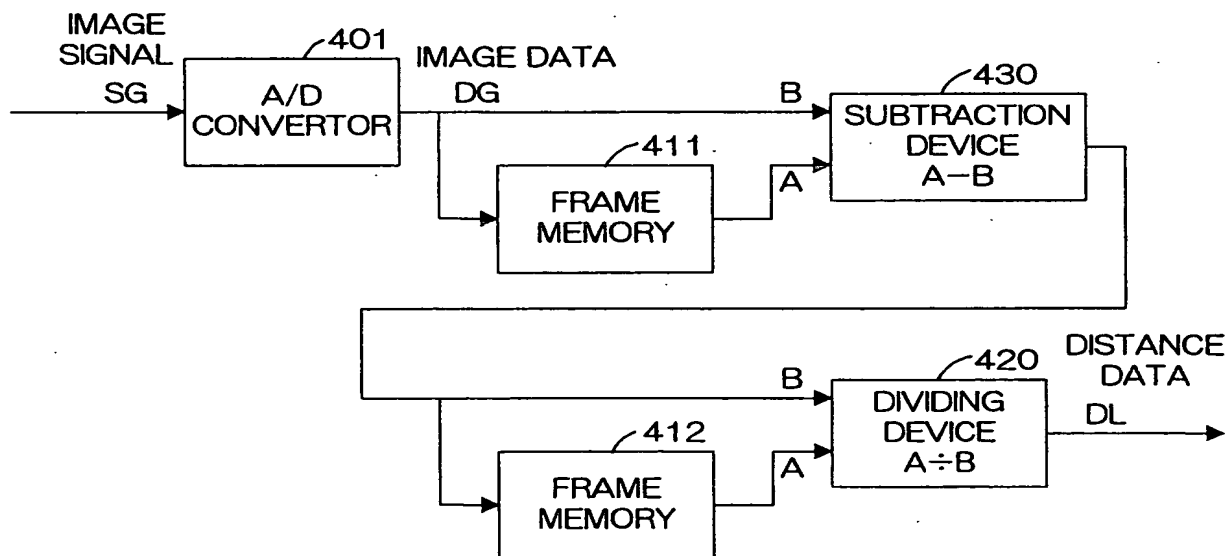


Fig.10(a)

	frame(n)	frame(n+1)	frame(n+2)	frame(n+3)
EMISSION CONTROL	INTERMITTENT EMISSION	INTERMITTENT EMISSION	INTERMITTENT EMISSION	INTERMITTENT EMISSION
EXPOSURE CONTROL	INTERMITTENT EXPOSURE	INTERMITTENT EXPOSURE	INTERMITTENT EXPOSURE	CONTINUOUS EXPOSURE

Fig.10(b)

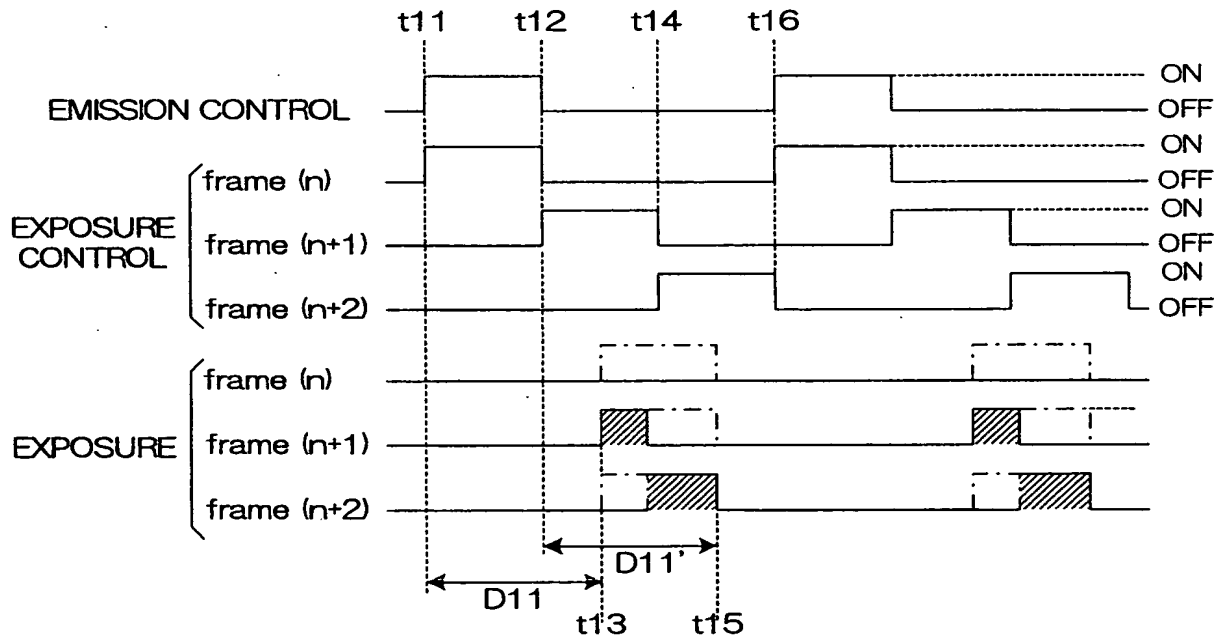


FIG.11

34c IMAGE PROCESSING CIRCUIT

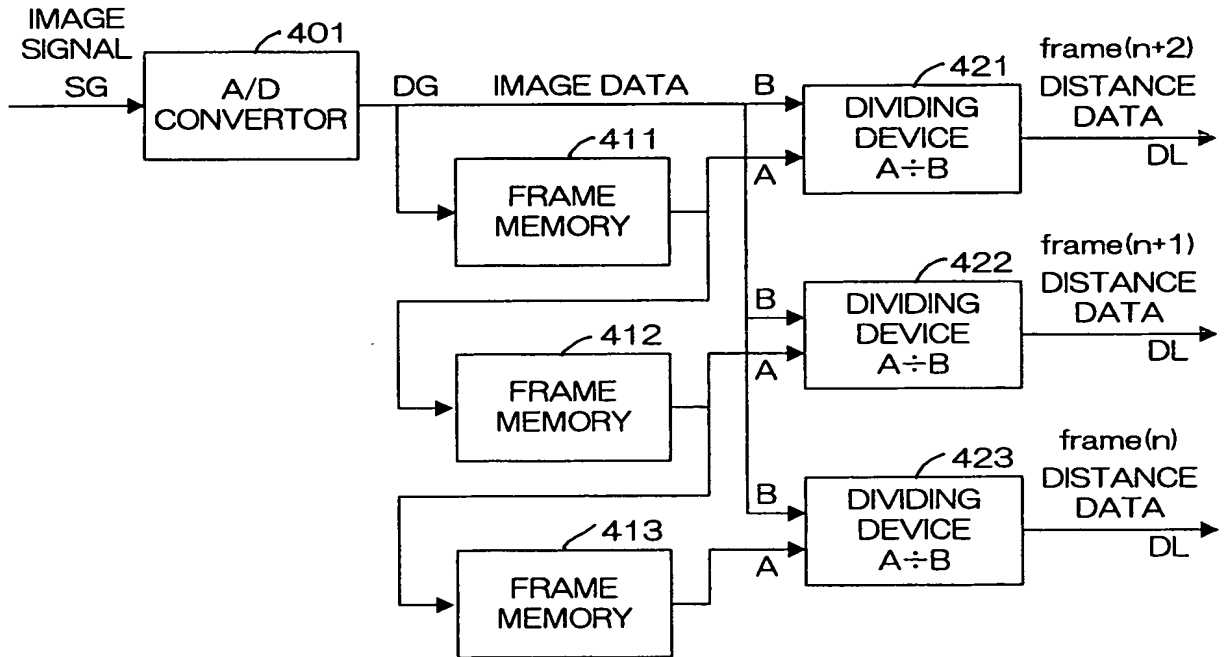


FIG.12

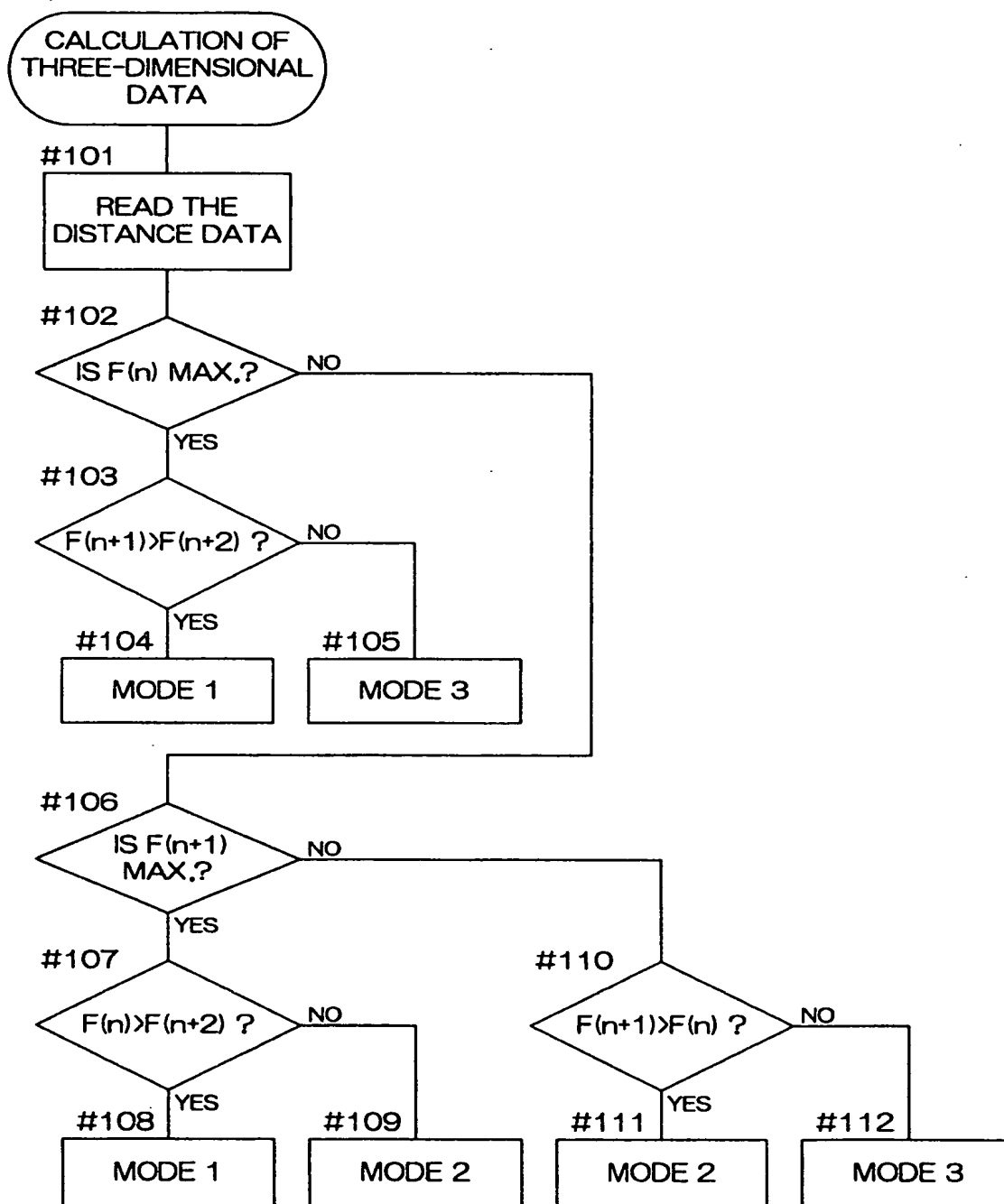


FIG.13

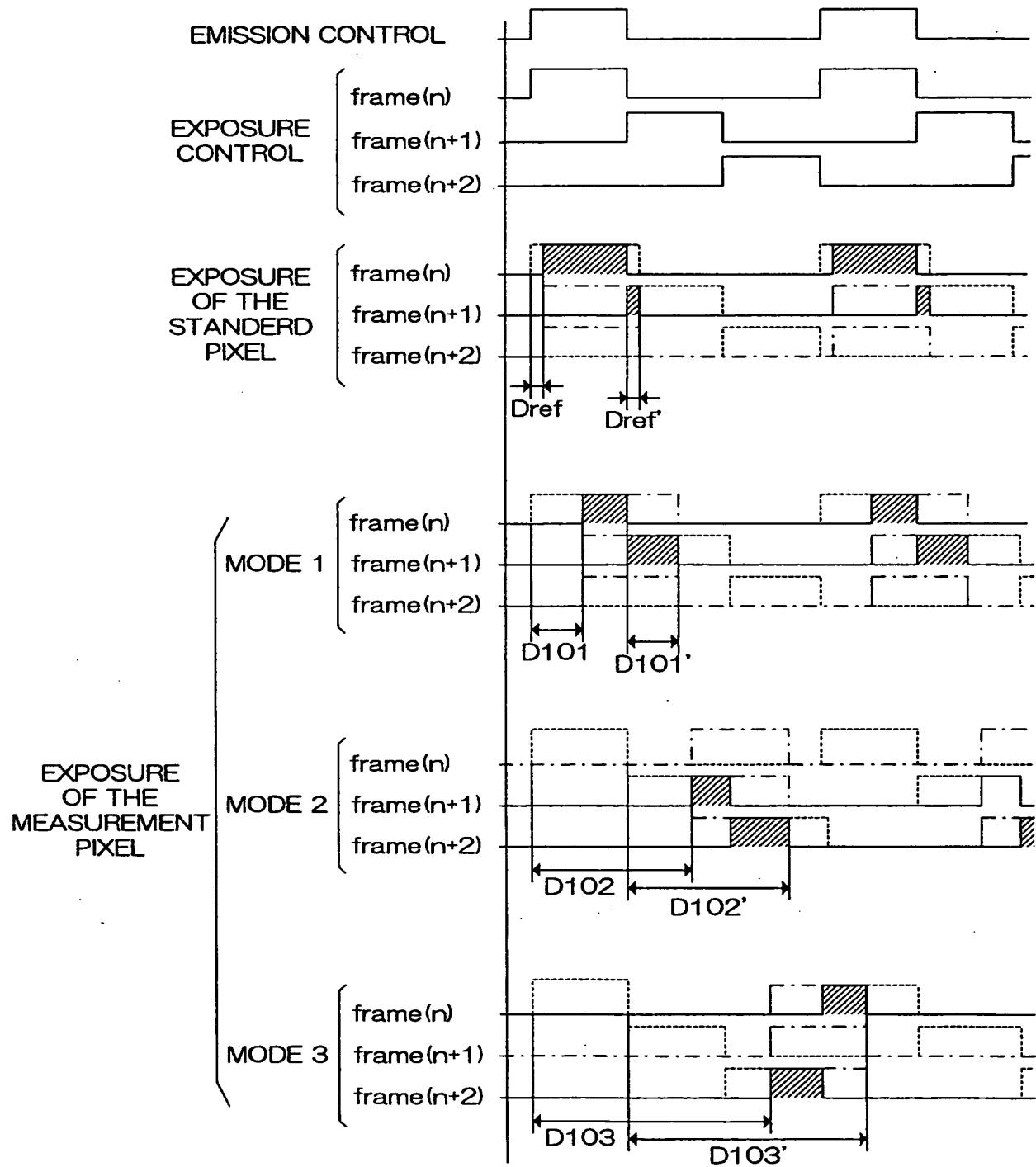


FIG.14(a)

	frame (n)	frame (n+1)	frame (n+2)	frame (n+3)	frame (n+4)	frame (n+5)	frame (n+6)
EMISSION CONTROL	INTER-MITTENT	INTER-MITTENT	INTER-MITTENT	INTER-MITTENT	INTER-MITTENT	INTER-MITTENT	INTER-MITTENT
EXPOSURE CONTROL	INTER-MITTENT	INTER-MITTENT	INTER-MITTENT	INTER-MITTENT	INTER-MITTENT	INTER-MITTENT	CONTINUOUS

FIG.14(b)

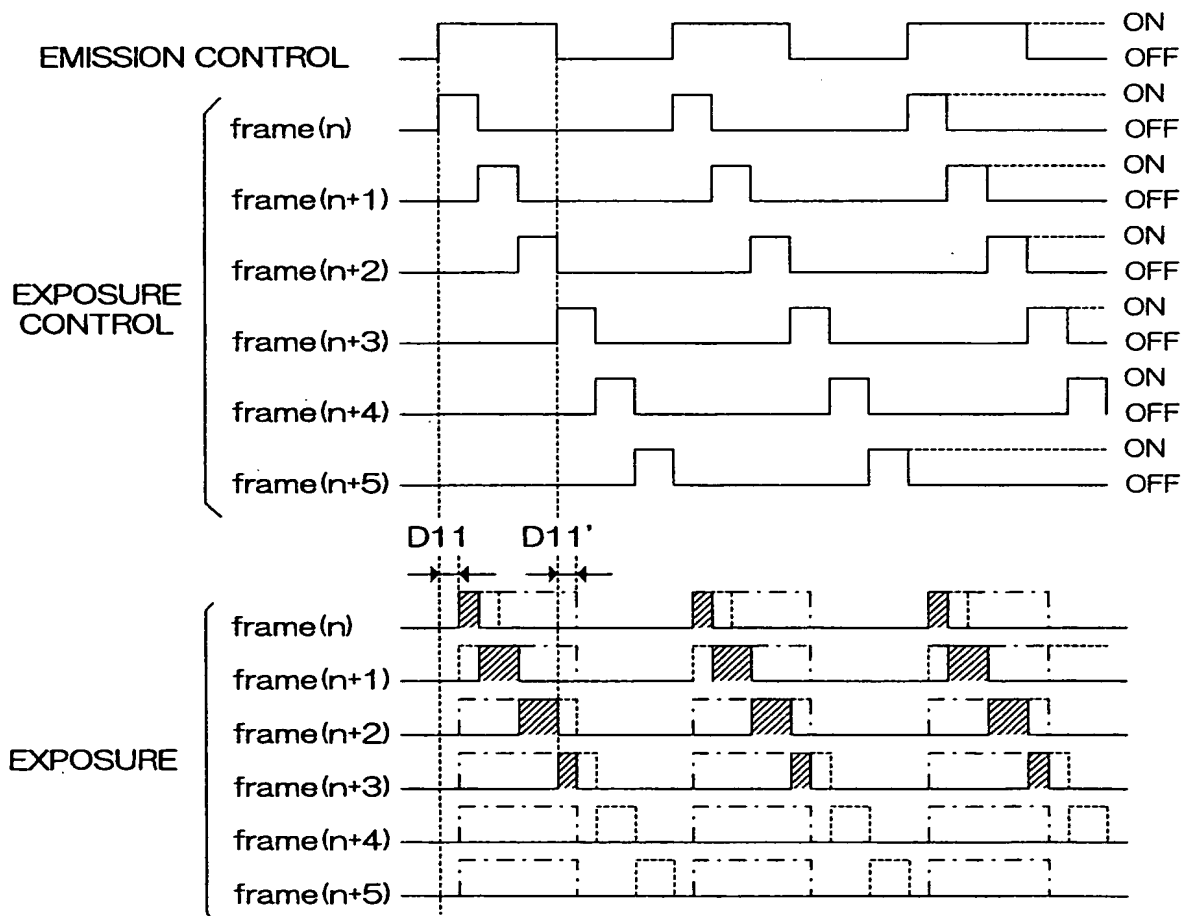


FIG.15

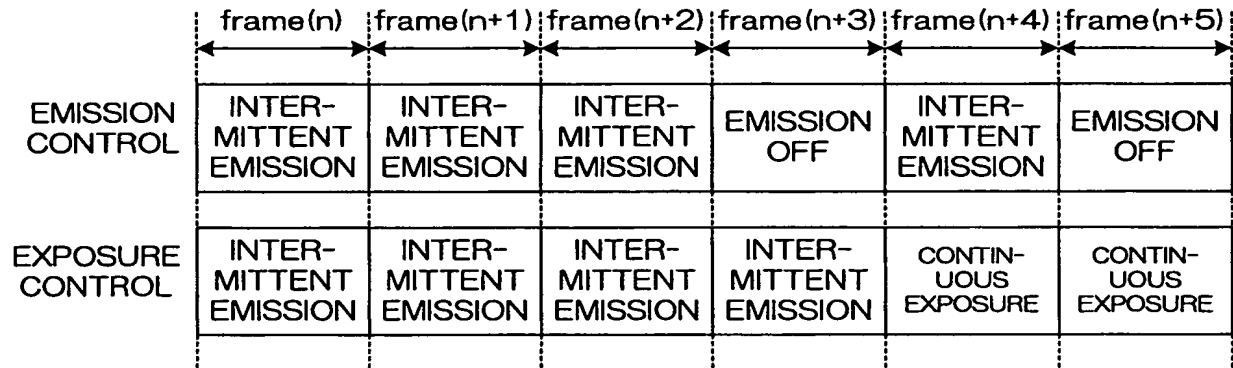


FIG.16

34d IMAGE PROCESSING CIRCUIT

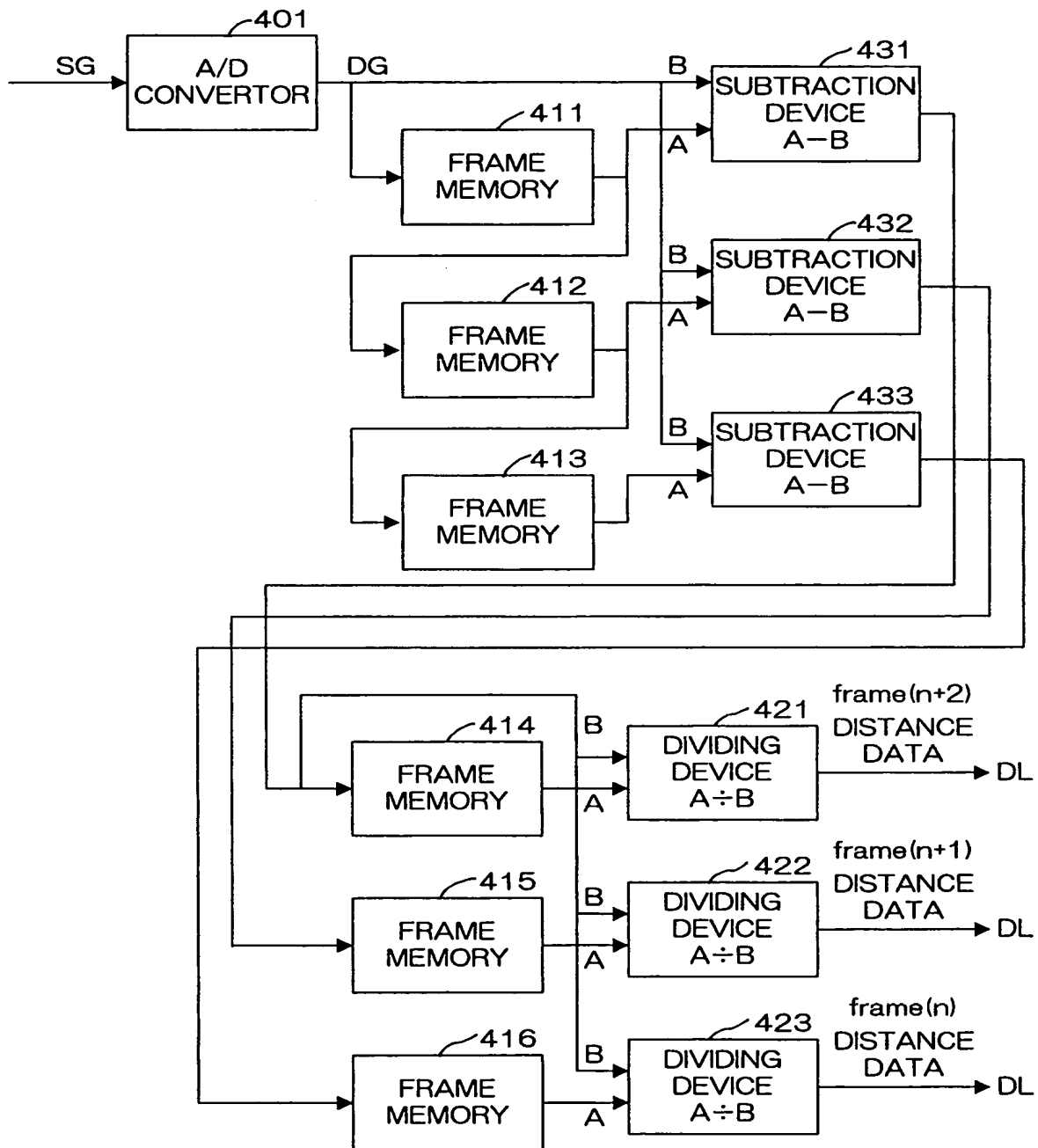


FIG.17(a)

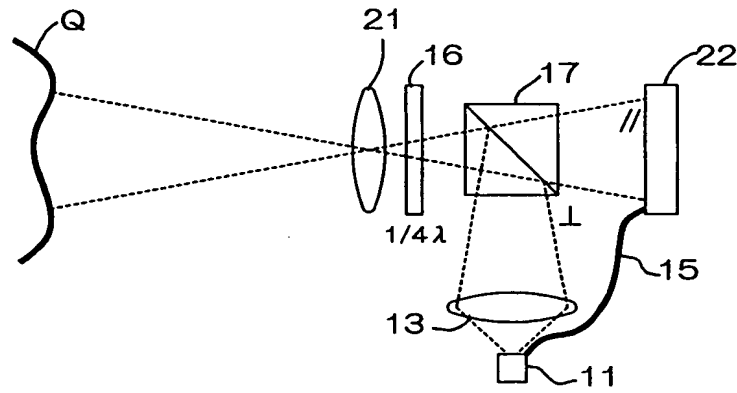


FIG.17(b)

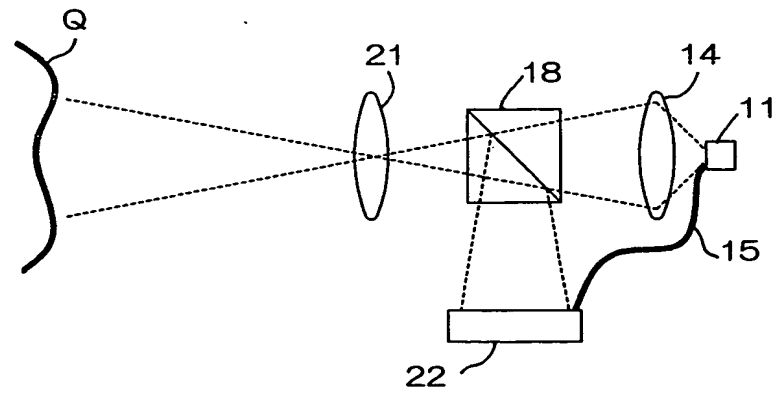


FIG.18

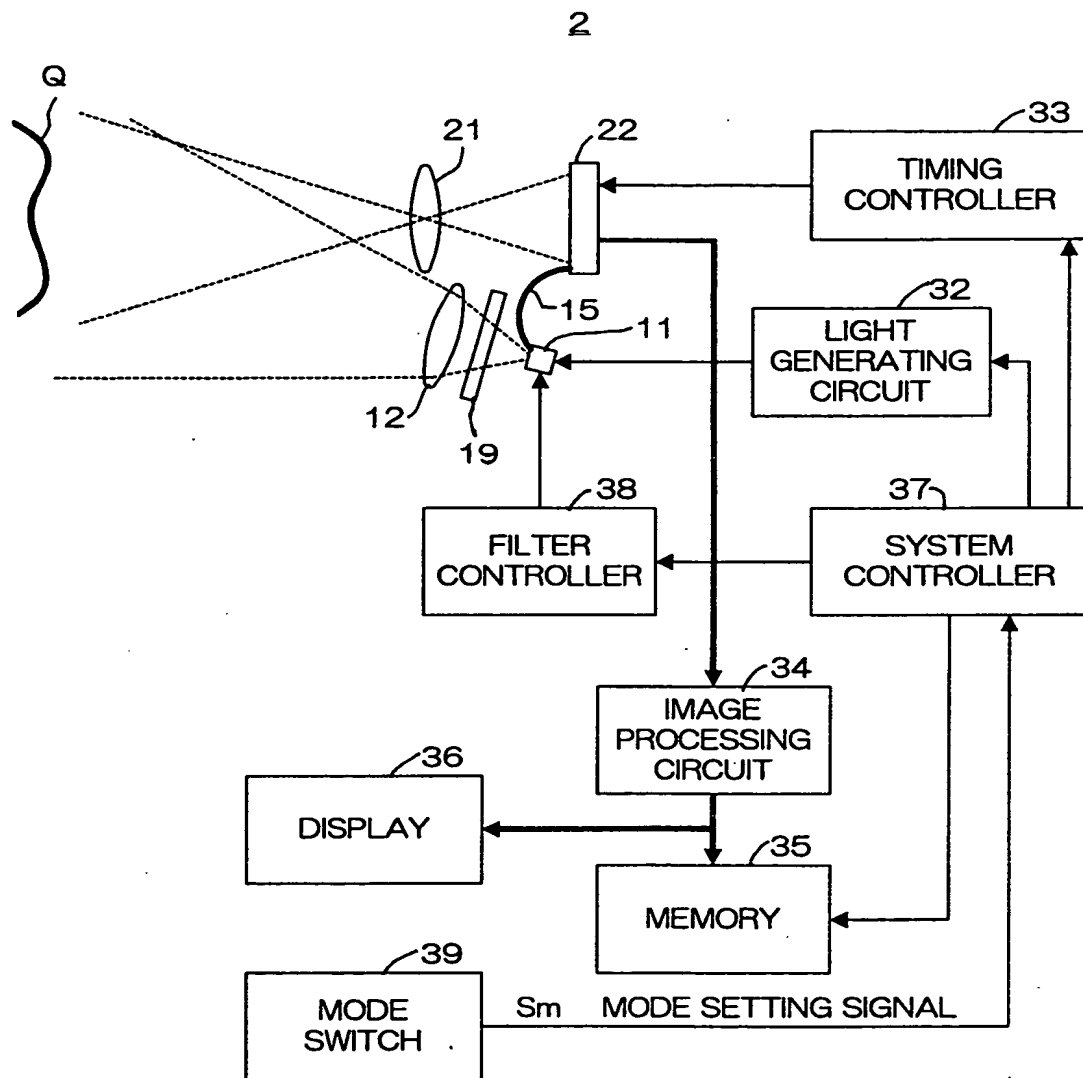


FIG.19(a)

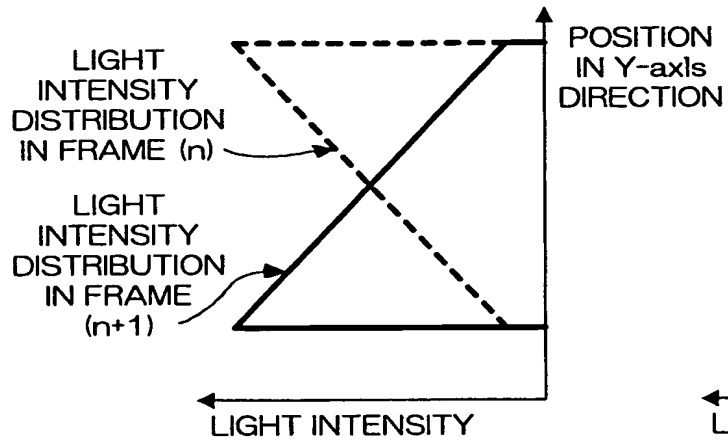


FIG.19(b)

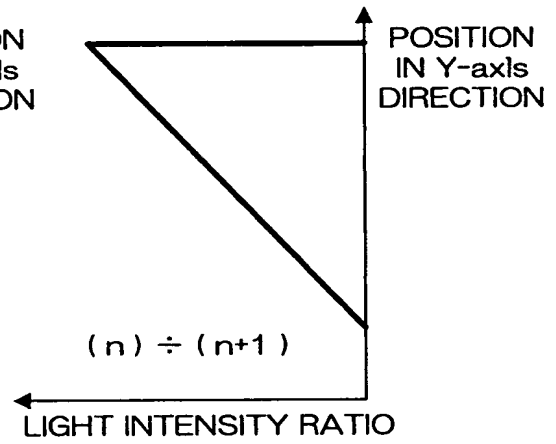


FIG.19(c)

